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DOCKET NO.: P05789 **PATENT**
CLIENT NO.: NATI15-05789
Customer No.: 23990

OIPE JC10 JUL 26 2004 PATENT & TRADEMARK OFFICE

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of	:	AJAY KUMAR
U.S. Serial No.	:	10/686,101
Filed	:	October 15, 2003
For	:	METHOD AND SYSTEM FOR TUNING QUALITY FACTOR IN HIGH-Q, HIGH-FREQUENCY FILTERS
Group No.	:	2681
Examiner	:	Not Yet Assigned

MAIL STOP AMENDMENT
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

CERTIFICATE OF MAILING BY FIRST CLASS MAIL

The undersigned hereby certifies that the following documents:

1. Postcard receipt;
2. Information Disclosure Statement;
3. Form PTO/SB/08A and PTO/SB/08B; and
4. Eleven (11) references.

relating to the above application, were deposited as "First Class Mail" with the United States Postal Service, addressed to, MAIL STOP AMENDMENT, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on July 22, 2004.

Date: 7/22/04 Kathy Ceder
Mailer

Date: July 22, 2004 William A. Munck
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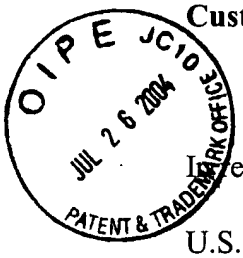

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P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

INFORMATION DISCLOSURE STATEMENT

Pursuant to the duty of disclosure under 37 C.F.R. § 1.56, Applicant submits this statement. This submittal is made in accordance with 37 C.F.R. §§ 1.97 and 1.98 and § 609 of the Manual of Patent Examining Procedure. The patents and publications herein are listed below and on the attached Forms PTO/SB/08A and PTO/SB/08B. Copies of the listed patents and publications are submitted herewith.

U.S. Patent No.	Inventor	Date
5,945,889	Shanthi-Pavan et al.	August 31, 1999
6,285,865	Vorenkamp et al.	September 4, 2001
6,377,315	Carr et al.	April 23, 2002
6,549,766	Vorenkamp et al.	April 15, 2003

Publications

H. Elhallabi et al., "High Frequency CMOS GM-C Bandpass Filter with Automatic On-Chip Tuning", PolySTIM Neurotechnology Laboratory, Department of Electrical and Computer Engineering, 2001 IEEE, Pp. 823-826.

Aydin Iker Karsilayan, "A Novel Automatic Tuning Scheme for High-Frequency High-Q Continuous-Time Filters", Texas A&M University, Department of Electrical Engineering, 2001 IEEE, Pp. 126-130.

Hengsheng Liu et al., "A High Frequency Bandpass Continuous-Time Filter with Automatic Frequency and Q-Factor Tuning", Texas A&M University, Department of Electrical Engineering, 2001 IEEE, Pp. I-328-I-331.

Juan Ignacio Osa et al., "MOSFET-C Filter with On-Chip Tuning and Wide Programming Range", IEEE Transactions on Circuits and Systems-II: Analog and Digital Signal Processing, Vol. 48, No. 10, October 2001, Pp. 944-951.

Hiroshi Yamazaki et al., "An Accurate Center Frequency Tuning Scheme for 450-kHz CMOS G_m-C Bandpass Filters", IEEE Journal of Solid-State Circuits, Vol. 34, No. 12, December 1999, Pp. 1691-1697.

Teemu Salo et al., "Direct Digital Tuning for Continuous-Time Filters", Proc. 43rd IEEE Midwest Symp. on Circuits and Systems, Aug. 8-11, 2000, Pp. 216-219.

Jose Silva-Martinez et al., "A 10.7-MHz 68-dB SNR CMOS Continuous-Time Filter with On-Chip Automatic Tuning", IEEE Journal of Solid-State Circuits, Vol. 27, No. 12, December 1992, Pp. 1843-1853.

Applicant hereby expressly reserves the right to swear behind the effective dates of any of the above Patents and to question the relevance and materiality of the Patents and Publications listed herein, in whole, in part, or in combination, subsequent to filing this Information Disclosure Statement.

Respectfully submitted,

DAVIS MUNCK, P.C.

Date: July 22, 2004

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PTO/SB/08A (08-03)
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Substitute for form 1449/PTO

INFORMATION DISCLOSURE STATEMENT BY APPLICANT

(Use as many sheets as necessary)

Sheet 1 of 2

Complete if Known	
Application Number	10/686,101
Filing Date	October 15, 2003
First Named Inventor	Ajay Kumar
Art Unit	2681
Examiner Name	Not Yet Assigned
Attorney Docket Number	P05789

OIPE JC10 JUL 26 2004 PATENT & TRADEMARK OFFICE

U. S. PATENT DOCUMENTS

Examiner Initials*	Cite No.[1]	Document Number Number-Kind Code[2] (if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
	AA	US- 5,945,889	08/31/1999	Shanthi-Pavan et al.	
	AB	US- 6,285,865	09/04/2001	Vorenkamp et al.	
	AC	US- 6,377,315	04/23/2002	Carr et al.	
	AD	US- 6,549,766	04/15/2003	Vorenkamp et al.	
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FOREIGN PATENT DOCUMENTS

Examiner Initials*	Cite No.[1]	Foreign Patent Document Country Code[3]-Number[4]-Kind Code[5] (if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages Or Relevant Figures Appear	T[6]

Examiner Signature		Date Considered	

*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant. [1] Applicant's unique citation designation number (optional). [2] See Kinds Codes of USPTO Patent Documents at www.uspto.gov or MPEP 901.04. [3] Enter Office that issued the document, by the two-letter code (WIPO Standard ST.3). [4] For Japanese patent documents, the indication of the year of the reign of the Emperor must precede the serial number of the patent document. [5] Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST.16 if possible. [6] Applicant is to place a check mark here if English language Translation is attached.

This collection of information is required by 37 CFR 1.97 and 1.98. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to take 2 hours to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. **SEND TO: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.**

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PTO/SB/08B (08-03)
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INFORMATION DISCLOSURE STATEMENT BY APPLICANT

(Use as many sheets as necessary)

Sheet 2 of 2

Complete if Known	
Application Number	10/686,101
Filing Date	October 15, 2003
First Named Inventor	Ajay Kumar
Art Unit	2681
Examiner Name	Not Yet Assigned
Attorney Docket Number	P05789

NON PATENT LITERATURE DOCUMENTS

Examiner Initials*	Cite No.[1]	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T[2]
	BA	H. Elhallabi et al., "High Frequency CMOS GM-C Bandpass Filter with Automatic On-Chip Tuning", PolySTIM Neurotechnology Laboratory, Department of Electrical and Computer Engineering, 2001 IEEE, Pp. 823-826.	
	BB	Aydin Iker Karsilayan, "A Novel Automatic Tuning Scheme for High-Frequency High-Q Continuous-Time Filters", Texas A&M University, Department of Electrical Engineering, 2001 IEEE. Pp. 126-130.	
	BC	Hengsheng Liu et al., "A High Frequency Bandpass Continuous-Time Filter with Automatic Frequency and Q-Factor Tuning", Texas A&M University, Department of Electrical Engineering, 2001 IEEE, Pp. I-328-I-331.	
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	BF	Teemu Salo et al., "Direct Digital Tuning for Continuous-Time Filters", Proc. 43rd IEEE Midwest Symp. on Circuits and Systems, Aug. 8-11, 2000, Pp. 216-219.	
	BG	Jose Silva-Martinez et al., "A 10.7-MHz 68-dB SNR CMOS Continuous-Time Filter with On-Chip Automatic Tuning", IEEE Journal of Solid-State Circuits, Vol. 27, No. 12, December 1992, Pp. 1843-1853.	

Examiner Signature		Date Considered	

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1 Applicant's unique citation designation number (optional). 2 Applicant is to place a check mark here if English language Translation is attached.

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